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12	GSI Technology, Inc.		
13	INITED CTAT	TEC DISTRICT COLIDT	
14	UNITED STATES DISTRICT COURT		
15	NORTHERN DISTRICT OF CALIFORNIA, SAN JOSE DIVISION		
16	GSI TECHNOLOGY, INC., a Delaware Corporation,	CASE NO. Civ. Action No. 13-CV-1081-PSG	
17 18	Plaintiff and Counter-Defendant, v.	DECLARATION OF PAUL CHIANG IN SUPPORT OF PLAINTIFF'S OPPOSITION TO MOTION FOR SUMMARY JUDGMENT OF DEFENDANT UNITED MEMORIES,	
19	UNITED MEMORIES, INC., a Colorado	INC.	
20	Corporation, and INTEGRATED SILICON SOLUTION, INC., a Delaware	Complaint: Filed March 8, 2013	
21	Corporation,	Courtroom: 5 Judge: Hon. Paul S. Grewal	
22	Defendants and Counter-Claimants.	Hearing Date: February 3, 2015 Hearing Time: 10:00 a.m.	
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A PIPER LIP (US)

I, Paul Chiang, hereby declare as follows:

- I am the Vice President of DRAM Design at GSI Technology, Inc. ("GSI").
 I have been involved in the memory chip design industry for 34 years.
- 2. I submit this declaration in support of Plaintiff's Opposition to Motion for Summary Judgment of Defendant United Memories, Inc. ("UMI").
- I make this declaration of my personal knowledge and, if called as a witness, I could and would testify competently to the matters set forth herein.
- 4. GSI paid UMI to design a 576Mb low latency DRAM ("LLDRAM") chip. GSI hired me to manage the development of the chip, and specifically, to be a primary point of contact with UMI. I have 29 years of experience in DRAM design.
- 5. On December 18, 2008, UMI delivered to GSI a database pertaining to a 576Mb LLDRAM chip. This type of database is sometimes referred to as a "layout database" or a "tape out database."
- 6. At the time UMI delivered the database in December 2008, UMI and GSI had a good business relationship. UMI and GSI were working together collaboratively to develop the 576Mb chip. I worked with UMI employees, such as Jon Faue and Larry Aldrich, and I trusted them. I believed that, as GSI's business partner, UMI would look out for GSI's interests.
- 7. I reviewed the database delivered by UMI. The database contained a compilation of materials including, among other things, all circuit schematics, schematics simulation modeling and parasitics, LVS hierarchy, signal/circuit matrix, pad locations, fuse locations, and full simulation files for the 576Mb LLDRAM chip. The database also contained a library, portions of which are necessary to develop the chip.
- 8. UMI made changes to the database, and delivered updated versions on December 20, 2008 and again in January and June of 2009.
- 9. I have worked with several memory chip design firms. In my experience in the memory chip industry, the items a design firm generally must deliver at the completion of a project include, at least, the layout, the circuit schematics, the library, schematics simulation modeling, and documents for test functions.

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